

**Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711103**

Project on: "Design and Development of System on Chip based next generation IoT System for Industry 4.0 with Functional safety and security"

[Sponsoring Authority: Chip to Startup (C2S), MeitY, Govt. of India]

**School of VLSI Technology
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711103**

Ref: Advt. No. VL 1954, dated 27.08.2026

[University Project Code: DRC/MEITY-C2S(4197)/VLSI/HR/005/23-24]

Interested candidates are requested to appear for walk in interview on **09.09.2026** at **11:00 AM**, for the following post in the **School of VLSI Technology, Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711103**.

Name of the post: Project Associate-I

No. of post – One (01)

Essential Qualification: M.Tech (having 1 years of experience in Analog and Mixed Signal Design) with 7.0 CGPA along with institute norms.

Desirable Qualification (s):

Experience with Analog and Mixed Signal Design, FPGA, Coding, chip tape-out and IC testing.

Duration: 2 years or until the completion of the project, subject to yearly evaluation of the candidate for the project execution.

Remuneration: INR 30,000/- per month (Consolidated)

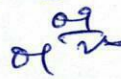
Age Limit: Maximum 35 years, (Upper age limit is relaxable up to 5 years for SC/ST/OBC/Woman and Physically handicapped candidates).

Interested eligible candidates should bring application letter in plain paper, recent bio-data, photo copies of the mark sheets, certificates, work experience certificate and a valid Identity proof. All documents should be self-attested. Documents will be verified at the time of joining. The selection will be cancelled if any discrepancies are found in the documents at the time of verification.

Venue and Date of the interview: 09.09.2026 School of VLSI Technology, IEST, Shibpur.

Note:

1. All applications must mention a valid e-mail id and phone number for further communication.
2. No TA/DA will be paid for attending the interview.


Asst Dean (R & C) 

(W. Code DRC-035/26-27)